

843.41127X00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: TANAKA, et al.

Serial No.: Not assigned

Filed: February 12, 2002

For: DIRECT-CONVERSION TRANSMITTING CIRCUIT AND
INTEGRATED TRANSMITTING/RECEIVING CIRCUIT



INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR 1.97 & 1.98

Assistant Commissioner for Patents February 12, 2002
Washington, D.C. 20231

Sir:

In the matter of the above-identified application, applicant(s) is submitting a copy of the document listed in the attached form equivalent to Form PTO-1449 for the Examiner's consideration.

This information disclosure statement is being submitted, with the New Application. Each of the documents listed on the attached form equivalent to Form PTO-1449 is in the English language.

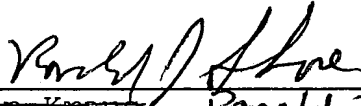
It is respectfully requested that this information disclosure statement be considered by the Examiner.

Please charge any shortage in the fees due in connection with the filing of this paper, including extension of time fees, to the deposit account of Antonelli, Terry, Stout &

Kraus Deposit Account No. 01-2135 (Case: 843.41127X00), and
please credit any excess fees to such deposit account.

Respectfully submitted,

ANTONELLI, TERRY, STOUT & KRAUS LLP


~~Melvin Kraus~~ Ronald J. Shore
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28,577

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Attachments

Form PTO-1449 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)	ATTY. DKT. NO. 843.41127X00	SERIAL NO. not assigned
	APPLICANT TANAKA, et al.	
	FILING DATE February 12, 2002	GROUP not assigned

U.S. PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date
AA						
AB						
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AE						
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AG						
AH						
AI						
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AL						

15929 U.S. PRO
10/07/02
02/12/02

FOREIGN PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation	
						Yes	No
AM							
AN							
AO							
AP							
AQ							
AR							
AS							
AT							

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

AU	"RF Circuits Technique of Dual-band Transceiver IC for GSM and DCS1800 application", TAKIKAWA, et al.; IEEE 25 th European Solid-State Circuits Conference; pp. 278-281; 1999
AV	
AW	
Examiner 	
Date Considered 	